

Figure 4.22 (a) Active area mask (b) Gate mask (c) Contact opening mask (d) Metal mask

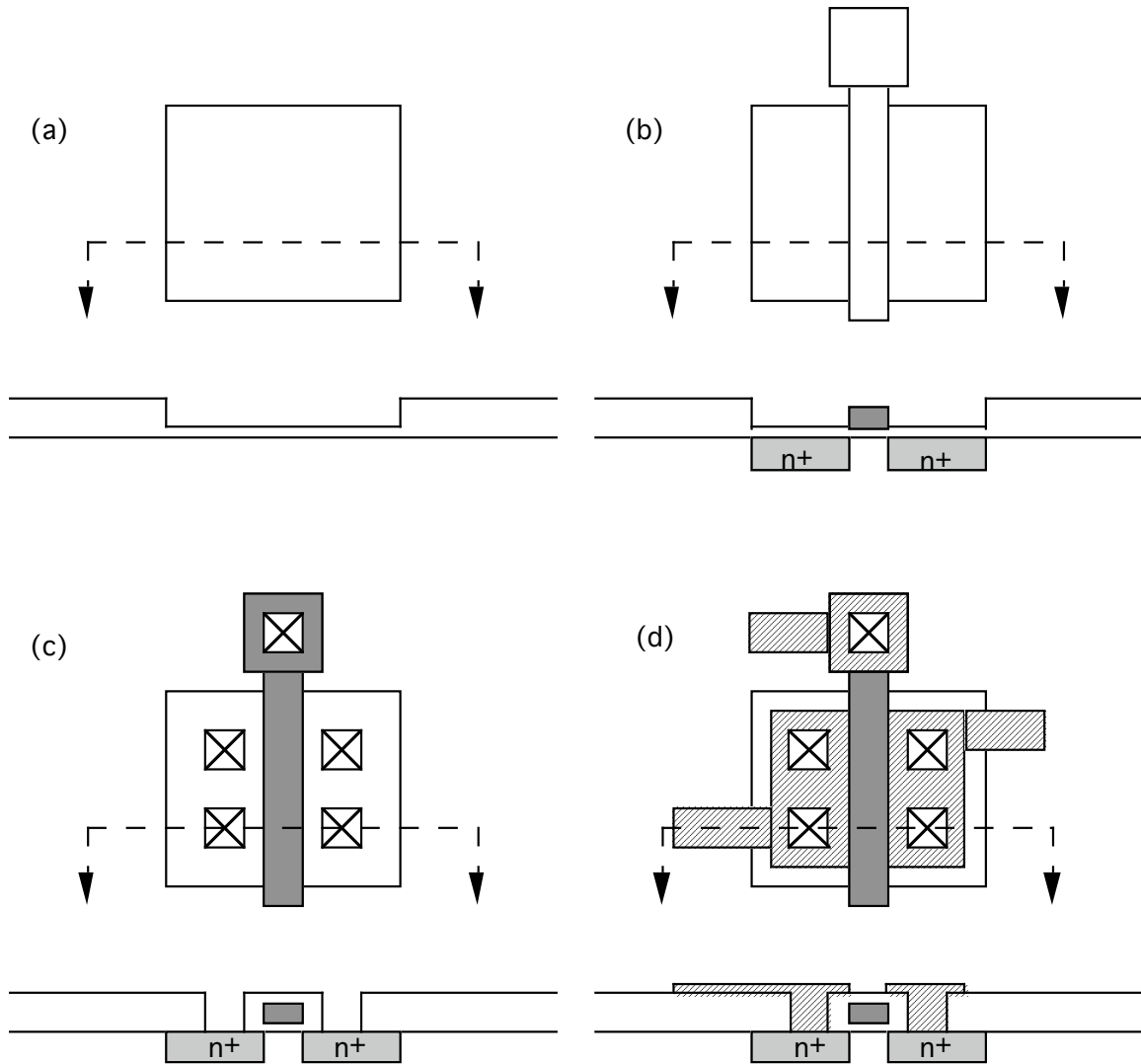


Figure 4.23 Composite top view and cross sections of a transistor with $W/L = 5/1$ demonstrating a basic set of ground rules.

